



NANO KOREA 2024

The 22nd International Nanotech Symposium & Exhibition



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Jiyoung Kim is a professor of Materials Science and Engineering and affiliated at Depts. of Electrical and Mechanical Engineering at the University of Texas at Dallas since 2005. He received the B.S. (1986) and the M.S. (1988) at Seoul National University, and the Ph.D. (1994) at the University of Texas at Austin. He worked as an integration engineer to develop multilevel interconnection for 0.5 and 0.35 μm technology nodes in worldwide productization at Texas Instrument, Inc (Dallas, TX) from 1994 to 1996. From 1996 to 2005 he was a faculty member of School of Advanced Materials Engineering at Kookmin University, (Seoul, Korea). He is an author or coauthor of more than 400 peer reviewed journal and proceeding papers. His H-index and number of citations are 56 and about more than, respectively. His research interests are in the area of future semiconductor technology particularly focusing on implementation of advanced ALD process, characterization and its applications for logic, memory and analog devices.